

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Jason Cheng; Cyrus Tsui; Satwant Singh; Albert Chan; Ju Shen; Clement Lee

Assignee: Lattice Semiconductor Corporation

Title: Programmable Logic Device With Enhanced Wide Input Product Term Cascading

Serial No.: 10/619,711 Filing Date: 07/14/2003

Examiner: Unknown Group Art Unit: 2651

Docket No.: M-9559-2P US

Irvine, California  
December 9, 2003

COMMISSIONER FOR PATENTS  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97**

Dear Sir:

Pursuant to 37 C.F.R. §1.97, Applicant wishes to call the following documents (copies enclosed) to the attention of the Examiner.

1. A PTO form 1449 listing these documents is enclosed.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, or
3. an admission that the information cited herein is, or is considered to be material to patentability as defined in §1.56(b).

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Applicant(s) believes that no fee is required for submission of this statement. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 50-2257. Please deduct any additional fees from, or credit any overpayment to the above-noted Deposit Account.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on December 9, 2003.



Eric Hoover

12/9/03  
Date of Signature

Respectfully submitted,



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U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.	Serial No.			
				M-9559-2P US	10/619,711			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT  <i>O P E</i> (Use several sheets if necessary)				Applicant(s)				
DEC 12 2003 <i>L C</i>				Jason Cheng et al.				
				Filing Date	Group			
				07/14/2003				
U.S. Patent Documents								
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
AA	5,977,791	11/02/1999	Veenstra					
AB	6,020,759	02/01/2000	Heile					
AC	6,029,236	02/22/2000	Steele et al.					
AD	6,064,599	05/16/2000	Cliff et al.					
AE	6,144,573	11/07/2000	Heile					
AF	6,150,838	11/21/2000	Wittig et al.					
AG	6,208,163	03/27/2001	Wittig et al.					
AH	6,215,326	04/10/2001	Jefferson et al.					
AI	6,292,017	09/18/2001	Rangasayee					
AJ	4,769,562	09/06/1988	Ghisio					
AK								
Foreign Patent Documents								
							Translation	
	Document	Date	Country	Class	Subclass		Yes	No
AL	EP0866558	09/23/1998	EP					
AM								
AN								
AO								
AP								
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ	U.S. Patent Publication No. 2001/0043082 A1, publication date 11/22/2001, to Wittig et al.						
	AR							
	AS							
Examiner		Date Considered						
<p><b>*EXAMINER:</b> Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								